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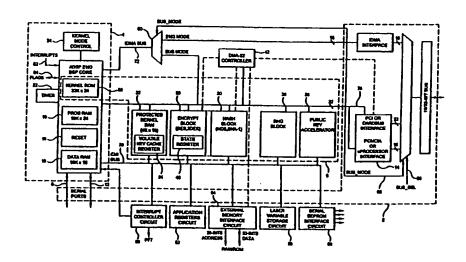
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With international search report.

Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

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(54) Title: CRYPTOGRAPHIC CO-PROCESSOR



(57) Abstract

A secure communication platform on an integrated circuit is a highly integrated security processor which incorporates a general purpose digital signal processor (DSP) (62), along with a number of high performance cryptographic function elements, as well as a PCI and PCMCIA (14) interface. The secure communications platform is integrated with an off-the-shelf DSP so that a vendor who is interested in digital signal processing could also receive built-in security functions which cooperate with the DSP. The integrated circuit includes a callable library of cryptographic commands and encryption algorithms. An encryption processor is included to perform key and data encryption, as well as a high performance hash processor and a public key accelerator (28).

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INTERNATIONAL SEARCH REPORT

International application No. PCT/US98/19316

A. CL	ASSIFICATION OF SUBJECT MATTER				
IPC(6)	:H04L 9/06				
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X Further	documents are listed in the continuation of Box	C. See patent family annex.			
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International application No. PCT/US98/19316

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Electronic data bases consulted (Name of data base and where practicable terms used):

APS, DIALOG

search terms: key recovery, key escrow, hash, smart card, token card, secure kernel, diffie hellman, public key, plural algorithm, encryption algorithm, multiple algorithm, random key, random number generator

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